

DIGITAL OSCILLOSCOPE

Patent Number: JP5264595
Publication date: 1993-10-12
Inventor(s): HAYASHI SHIGERU; others: 03
Applicant(s): MATSUSHITA ELECTRIC IND CO LTD
Requested Patent: [JP5264595](#)
Application Number: JP19920035411 19920221
Priority Number(s):
IPC Classification: G01R13/20
EC Classification:
Equivalents:

Abstract

PURPOSE: To enable overlap displaying of eye patterns and the like by masking the lower bit in the time base counter of a digital oscilloscope of vector display type.
CONSTITUTION: Analog input signals are A/D converted with an A/D converter 11, stored once in a data memory 12 and the data are transferred intermittently to an indication memory 13 under the control of a controller 17. For example in a case four times of taken waveforms are transferred to the indication memory 13, every four words are transferred at the first time and every four words with an addition of one address are transferred at the second time. The output of the indication memory 13, after passing a D/A converter 14 and amplifying with a main amplifier 15 are impressed to Y-axis of a CRT 16. A time base counter 18 and a mask register 19 are controlled with a controller 17 and a part of the time base counter 18 output is masked with a gate for mask 20, the output after passing a D/A converter 21 is amplified with a main amplifier 22 and impressed to X-axis of the CRT 16.

Data supplied from the [esp@cenet](#) database - I2